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APPLICANTS

Akio Kitamura, Nagano, JAPAN;

** CONTINUING DATA *****
 This application is a DIV of 10/085,632 02/27/2002 PAT 6,639,287 *PK* ✓

** FOREIGN APPLICATIONS *****
 JAPAN JP 2001-052099 02/27/2001 *PK* ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 27	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged	Examiner's Signature <i>[Signature]</i>	Initials <i>[Initials]</i>	

ADDRESS
 Marc A. Rossi
 ROSSI & ASSOCIATES
 P.O. Box 826
 Ashburn, VA
 20146-0826

TITLE
 METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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